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process are disclosed in a co-pending application serial number 09/933,607, entitled "METHOD AND SYSTEM FOR A HANDOFF IN A BROADCAST COMMUNICATION SYSTEM," filed on August 20, 2001, and assigned to the assignee of the present invention.

IN THE CLAIMS

Please add the claims as indicated below.

- 2 33. (NEW) An apparatus for reducing power consumption of a
subscriber station, comprising:
a processor; and
4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:
6 determine a number of frames that must be received correctly; and
cause termination of reception of the frames when said determined
8 number of frames was received correctly.

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- 2 34. (NEW) The apparatus as claimed in claim 33 wherein said
processor is configured to determine a number of frames that must be received
correctly by executing a set of instructions to:
4 determine an amount of redundancy; and
determine the number of frames that must be received correctly in
6 accordance with said determined amount of redundancy.

- 2 35. (NEW) The apparatus as claimed in claim 34 wherein said
processor is configured to determine an amount of redundancy by executing a
set of instructions to:
4 determine the amount of redundancy independently of the received
frames.

2 36. (NEW) The apparatus as claimed in claim 34 wherein said
processor is configured to determine an amount of redundancy by executing a
set of instructions to:

4 determine an encoding rate of received frames; and
determine the amount of redundancy in accordance with the encoding
6 rate.

2 37. (NEW) The apparatus as claimed in claim 34 wherein said
processor is configured to determine the number of frames that must be
received correctly in accordance with said determined amount of redundancy by
4 executing a set of instructions to:

determine a minimum number of frames that must be received correctly.

2 38. (NEW) The apparatus as claimed in claim 37 wherein said
processor is further configured to execute a set of instructions to:

4 increase said determined minimum number of frames that must be
received correctly by a first number.

2 39. (NEW) The apparatus as claimed in claim 36 wherein said
processor is configured to determine an encoding rate of received frames by
executing a set of instructions to:

4 determine an encoding rate of received frames in accordance with the
received frames.

2 40. (NEW) The apparatus as claimed in claim 36 wherein said
processor is configured to determine an encoding rate of received frames by
executing a set of instructions to:

4 determine an encoding rate of received frames independently of the
received frames.

2 41. (NEW) The apparatus as claimed in claim 33 wherein said
processor is configured to cause termination of reception of the frames when

4 said determined number of frames was received correctly by executing a set of instructions to:

6 cause termination of reception of the frames when said determined number of frames was received correctly and a time during which the subscriber station is obligated to receive the frames expired.

2 42. (NEW) An apparatus for performing hard handoff on a common broadcast channel comprising:

a processor; and

4 a storage medium communicatively coupled to said processor and comprising a set of instructions executable by said processor to:

6 cause a subscriber station to receive frames transmitted on a common broadcast channel from a first sector;

8 determine a need for handoff;

10 identify at least one sector belonging to a soft handoff group different from a soft handoff group including the first sector;

12 determine a number of frames from a current buffer that must be received correctly;

14 cause the subscriber station to terminate reception of the frames when said determined number of frames was received correctly; and

16 cause the subscriber station to begin reception of frames from the identified at least one sector.

2 43. (NEW) The apparatus as claimed in claim 42 wherein said processor is configured to determine a number of frames that must be received correctly by executing a set of instructions to:

4 determine an amount of redundancy; and

6 determine the number of frames that must be received correctly in accordance with said determined amount of redundancy.

2 44. (NEW) The apparatus as claimed in claim 43 wherein said processor is configured to determine an amount of redundancy by executing a set of instructions to:

4 determine the amount of redundancy independently of the received
frames.

45. (NEW) The apparatus as claimed in claim 43 wherein said
2 processor is configured to determine an amount of redundancy by executing a
set of instructions to:

4 determine an encoding rate of received frames; and
determine the amount of redundancy in accordance with the encoding
6 rate.

46. (NEW) The apparatus as claimed in claim 43 wherein said
2 processor is configured to determine the number of frames that must be
received correctly in accordance with said determined amount of redundancy by
4 executing a set of instructions to:

determine a minimum number of frames that must be received correctly.

47. (NEW) The apparatus as claimed in claim 46 wherein said
2 processor is further configured to execute a set of instructions to:

increase said determined minimum number of frames that must be
4 received correctly by a first number.

48. (NEW) The apparatus as claimed in claim 45 wherein said
2 processor is configured to determine an encoding rate of received frames by
executing a set of instructions to:

4 determine an encoding rate of received frames in accordance with the
received frames.

49. (NEW) The apparatus as claimed in claim 45 wherein said
2 processor is configured to determine an encoding rate of received frames by
executing a set of instructions to:

4 determine an encoding rate of received frames independently of the
received frames.

2 50. (NEW) The apparatus as claimed in claim 42 wherein said
processor is configured to cause the subscriber station to terminate reception of
the frames when said determined number of frames were received correctly by
4 executing a set of instructions to:

cause the subscriber station to terminate reception of the frames when
6 said determined number of frames were received correctly and a time during
which the subscriber station is obligated to receive the frames expired.

2 51. (NEW) The apparatus as claimed in claim 42 wherein said
processor is further configured to execute a set of instructions to:

determine whether at least some decoded packets received from the at
4 least one sector are identical to at least some decoded packets received from
the first sector; and

6 discard the identical packets.

2 52. (NEW) An apparatus for a handoff from an area covered by
an origination system into an area covered by a destination system comprising:

a processor; and

4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:

6 cause a subscriber station to receive service on a channel from a
sector in the origination system;

8 determine a need for handoff;

identify a destination system;

10 determine a number of frames from a current buffer that must be
received correctly;

12 cause the subscriber station to terminate reception of the frames
when said determined number of frames were received correctly;

14 cause the subscriber station to tune to a frequency of the
destination system; and

16 cause the subscriber station to receive service on a channel from
at least one sector if the at least one sector of the destination system is
18 acquired at the subscriber station.

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53. (NEW) The apparatus as claimed in claim 52 wherein said
2 processor is further configured to execute a set of instructions to:
determine a time to cause the subscriber station to restart receiving
4 service on the channel from the sector in the origination system.

54. (NEW) The apparatus as claimed in claim 52 wherein said
2 processor is further configured to execute a set of instructions to:
store signals received at the frequency of the destination system;
4 cause the subscriber station to retune to a frequency of the
origination system and receive service on the channel from the sector in
6 the origination system; and
analyze the stored signals to identify a sector in a destination
8 system that can provide service;
if no sector of the destination system is acquired at the subscriber
10 station.

55. (NEW) The apparatus as claimed in claim 54 wherein said
2 processor is configured to cause the subscriber station to retune to a frequency
of the origination system and receive service on the channel from the sector in
4 the origination system by executing a set of instructions to:
cause the subscriber station to retune to the frequency of the origination
6 system before the time to restart receiving service on a channel from a sector in
the origination system

56. (NEW) The apparatus as claimed in claim 54 wherein said
2 processor is further configured to execute a set of instructions to:
cause the subscriber station to perform hard handoff if the sector in a
4 destination system is identified.

57. (NEW) An apparatus for utilizing a common broadcast
2 channel for signaling, comprising:
a processor; and

4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:
6 replace part of a content of a parity portion of a transmitting buffer
with a signaling information; and
8 cause a transmission of a content of the transmitting buffer at a
determined time on the common broadcast channel.

58. (NEW) The apparatus as claimed in claim 57 wherein said
2 processor is further configured to execute a set of instructions to:
cause increase of power for transmission of the common broadcast
4 channel during the determined time.

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59. (NEW) A apparatus for utilizing a common broadcast
2 channel for signaling, comprising:
a processor; and
4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:
6 encode a content of a systematic portion of a transmitting buffer
with a first code to provide parity bits into a first part of a parity portion of
8 the transmitting buffer;
add signaling information into a second part of the parity portion of
10 the transmitting buffer, the second part being different from the first part;
cause transmission of a content of the transmitting buffer at a
12 determined time on the common broadcast channel.

60. (NEW) The apparatus as claimed in claim 59 wherein said
2 processor is further configured to execute a set of instructions to:
encode a content in the systematic portion of the transmitting buffer with
4 a second code to provide parity bits into the parity portion of the transmitting
buffer; and
6 cause transmission of the content of the transmitting buffer at other than
the determined time on the common broadcast channel.

2 61. (NEW) The apparatus as claimed in claim 59 wherein said
processor is further configured to execute a set of instructions to:
4 cause increase of power for transmission of the common broadcast
channel during the determined time.

2 62. (NEW) An apparatus for utilizing a common broadcast
channel for signaling, comprising:
a processor; and
4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:
6 provide frames received on the common broadcast channel to a
receiving buffer;
8 decode the receiving buffer with a first code if the frames were
received in error during a determined time; and
10 decode the receiving buffer with a second code if the frames were
received in error during other than the determined time.

2 63. (NEW) An apparatus for utilizing a common broadcast
channel for signaling, comprising:
a processor; and
4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:
6 encode a packet containing channel content information with a
first code;
8 encode a packet containing channel content information and
signaling information with a second code; and
10 cause transmission of said encoded packets.

2 64. (NEW) An apparatus for utilizing a common broadcast
channel for signaling, comprising:
a processor; and
4 a storage medium communicatively coupled to said processor and
comprising a set of instructions executable by said processor to:

6 decode received packet in accordance with a first rate hypothesis;
and
8 decode received packet in accordance with a second rate
hypothesis if said decoding received packet in accordance with a first
10 rate hypothesis was unsuccessful.

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